

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
21 July 2005 (21.07.2005)

PCT

(10) International Publication Number  
**WO 2005/067290 A3**

(51) International Patent Classification<sup>7</sup>: **H04N 5/76**

(21) International Application Number:  
PCT/US2004/043080

(22) International Filing Date:  
24 December 2004 (24.12.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
10/750,544 31 December 2003 (31.12.2003) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

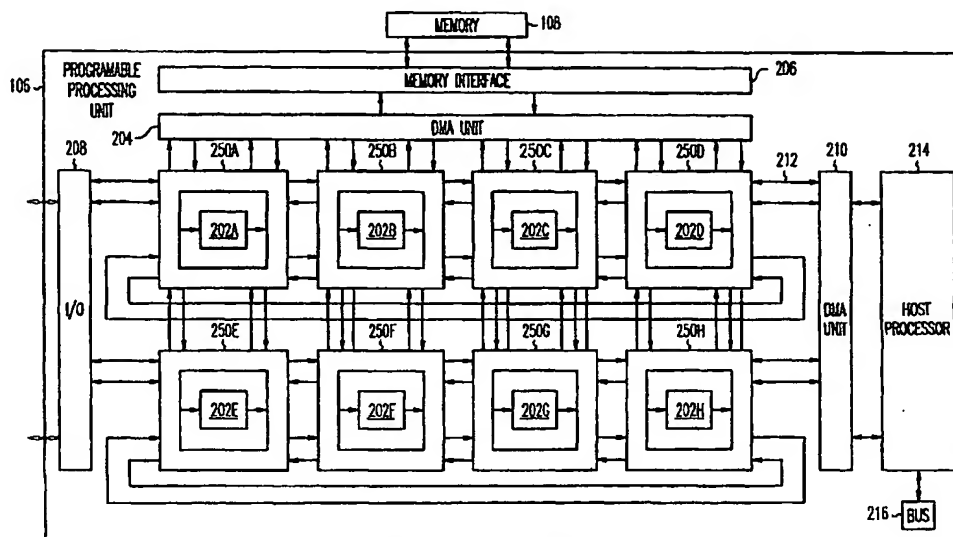
Published:

— with international search report  
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

(88) Date of publication of the international search report:  
8 December 2005

[Continued on next page]

(54) Title: PROGRAMMABLE VIDEO PROCESSING AND VIDEO STORAGE ARCHITECTURE



(57) Abstract: In an embodiment, a method includes receiving video into a video display device. The method also includes storing, by at least one processor, the video into a memory, upon determining that the video display device is in a storage mode. Additionally, the method includes performing enhanced image processing on the video with the at least one processor, upon determining that the video display device is in an image processing mode.

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# INTERNATIONAL SEARCH REPORT

International Application No

PCT/US2004/043080

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 H04N5/76

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X  A	<p>EP 1 324 604 A (SONY CORPORATION) 2 July 2003 (2003-07-02)</p> <p>paragraph '0131! - paragraph '0134! paragraph '0138! paragraph '0154! paragraph '0159! paragraph '0397! - paragraph '0401! paragraph '0419! paragraph '0441! paragraph '0446! - paragraph '0449! paragraph '0470! paragraph '0478! paragraph '0480! figures 12,57,59</p> <p style="text-align: center;">— — — — — — / —</p>	<p>1-7, 9-11,13, 19-29 8,12, 14-18,30</p>

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

6 September 2005

Date of mailing of the international search report

18/10/2005

Name and mailing address of the ISA

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# INTERNATIONAL SEARCH REPORT

International Application No

PCT/US2004/043080

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A	US 5 689 647 A (MIURA ET AL) 18 November 1997 (1997-11-18) column 6, line 35 - line 39 column 11, line 36 - line 65	1-30
A	BROFFERIO S ET AL: "PERFORMANCE EVALUATION OF IMAGE PARTITIONED PROCESSING ON TRANSPUTER NETWORK" PROCEEDINGS OF THE IASTED INTERNATIONAL SYMPOSIUM. APPLIED INFORMATICS, February 1989 (1989-02), pages 183-185, XP000791029 the whole document	1-30
A	WO 99/23817 A (INTEL CORPORATION; DUNTON, RANDY, R; METZ, WERNER; CORUM, CURTIS, A; B) 14 May 1999 (1999-05-14) page 9, paragraph 2 - paragraph 3 page 10, paragraph 1 - paragraph 3 page 11, paragraph 3 figure 2	1-30
A	US 2003/208638 A1 (ABRAMS THOMAS ALGIE ET AL) 6 November 2003 (2003-11-06) paragraph '0046! - paragraph '0047! paragraph '0052! paragraph '0056! paragraph '0062! - paragraph '0063! figures 2,10,12	1-30

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Information on patent family members

International Application No

PCT/US2004/043080

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